FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 32079/2:7	APPLICATION NO. 09/174,057
	INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)	APPLICANT Rubinstein, Richard	
		FILING DATE 10/16/98	GROUP 2785

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
HK	AA	5 8 4 1 4 4 4	11/24/98	Mun	345	506	
	AB	5 6 5 9 5 4 3	8/19/97	Ater	370	258	
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FOREIGN PATENT DOCUMENTS

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HK	AL	DE19713178 A1	01/10/98	Germany	G06F	12/08		X
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HK	AT	Article by Gordon Lang et al, AN OPTIMUM PARALLEL ARCHITECTURE FOR HIGH-SPEED REAL-TIME DIGITAL SIGNAL PROCESSING, 2/88

EXAMINER Harold K	DATE CONSIDERED 6/17/2000

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HK	AT	Article by A.C. Davies et al, INTERFACING A HARDWARE MULTIPLIER TO A GENERAL PURPOSE MICROPROCESSOR, 10/77

EXAMINER *Harold K.*	DATE CONSIDERED 6/19/2000

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